

<b>FORM PTO-1449 (SUBSTITUTE)</b>  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANT</b> (37 CFR 1.98(b))				Attorney Docket No.: P2001,0099 Appl. No.  Applicant GUNNAR KRAUSE ET AL.  Filing Date February 14, 2002 Group Art Unit			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
<i>cu</i>	A	5,971,923	10/26/99	Finger			
	B						
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<b>FOREIGN PATENT DOCUMENT</b>							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
	J						
	K						
	L						
	M						
	N						
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
<i>cu</i>		Ikeda, H.: "High-Speed DRAM Architecture Development", IEEE, Vol. 34, No. 5, May 1999, pp. 685-692					
<i>cu</i>		"DDR SDRAM Functionality and Controller Read Data Capture", Micron Technology Inc., Vol. 8, Issue 3, pp. 1-24					
<b>EXAMINER</b> <i>Wang</i>				<b>DATE CONSIDERED</b> <i>1-26-05</i>			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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